

**AMENDMENTS TO THE SPECIFICATION:**

**PI as replace the paragraph beginning on line 16, page 6 as follows:**

The timing changing circuit 10 has a fuse circuit 18 and decoders 20, 22. The fuse circuit 18 receives a test activating signal TESTX and test signals TEST0, TEST1. In accordance with these signals ~~and the states of the fuses implemented~~, the fuse circuit 18 outputs ~~adjusting signals RS0, RS1 and~~ adjusting signals VG0, VG1 to the decoder[[s]] ~~20 and 22, respectively.~~ In accordance with the states of the fuses implemented, the fuse circuit 18 outputs adjusting signals RS0, RS1 to the decoder 20. The decoder 20 receives the adjusting signals RS0 and RS1, and activates any one of decoding signals DRS0, DRS1, DRS2, and DRS3 in accordance with the signals received. The decoder 22 receives the adjusting signals VG0 and VG1, and activates any one of decoding signals DVG0, DVG1, DVG2, and DVG3 in accordance with the signals received.